

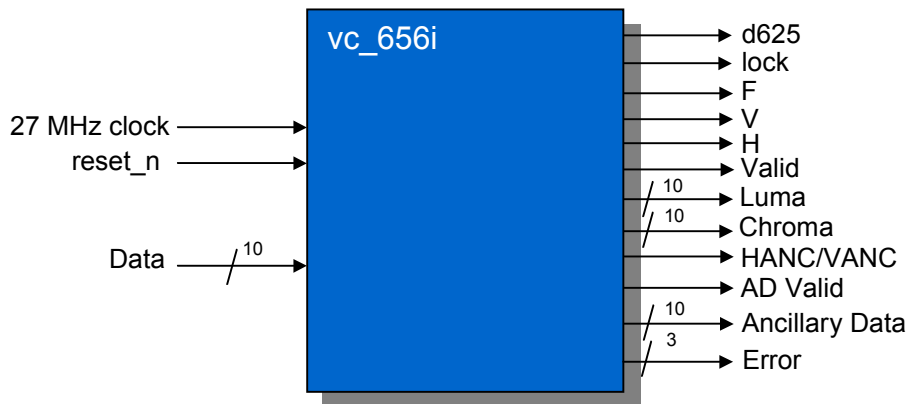


ITU 656-4 Decoder : vc_656i

This decoder allows designs to connect to devices that send 10-bit wide 27 MHz Standard Definition media streams with embedded syncs and auto-detects 525-line or 625-line video.

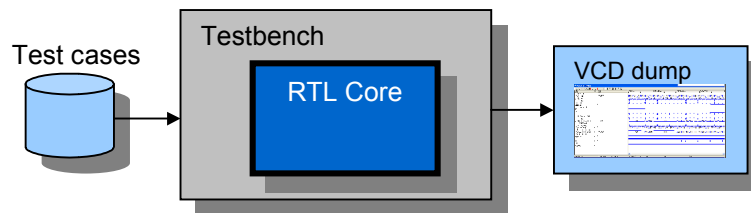
FEATURES

- Decodes ITU-R 656-4 parallel streams
- Extracts Luma, Chroma and Ancillary Data
- Extracts F, V and H synchronization signals
- Checks for possible errors in parallel stream
- Signals 625 line detection
- HANC/VANC, Valid Ancillary Data signaling
- Linted and Verified Verilog 2001
- Permanent Company License (not per-project)
- Targeting Actel, Altera, Lattice and Xilinx FPGAs
- ASIC or FPGA Applications



DELIVERABLES

- Synthesizable Verilog 2001 RTL Source Code
- Fast Verilog 2001 Testbench and Test cases
- VCD Functional Dump
- User Manual



APPLICATIONS

- Digital Cameras
- LCD Monitors and Televisions
- Portable Video Devices
- Rack-mounted Media Systems



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